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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/605,427	09/30/2003	En-Hsiang Yeh	MTKP0065USA	2426
27765	7590	04/04/2005	EXAMINER	
NORTH AMERICA INTERNATIONAL PATENT OFFICE (NAIPC)				CHOE, HENRY
P.O. BOX 506				
MERRIFIELD, VA 22116				
ART UNIT		PAPER NUMBER		
		2817		

DATE MAILED: 04/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/605,427	YEH, EN-HSIANG
Examiner	Art Unit	
Henry K. Choe	2817	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 01 February 2005.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-22 is/are pending in the application.
4a) Of the above claim(s) 14-22 is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1-13 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 30 September 2003 is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 12/30/04.

4) Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____ .
5) Notice of Informal Patent Application (PTO-152)
6) Other: ____ .

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-10 are rejected under 35 U.S.C. 102(e) as being anticipated by Mehr (Fig. 1a).

Regarding claims 1, 8 and 9, Mehr (Fig. 1a) discloses an amplifier circuit comprising an input port (V_{ip}, V_{in}) which receives an input signal (a signal coming into the input terminal V_{ip}, V_{in}), a gain circuit (114) which amplifies the input signal by corresponding gain ratios in various gain modes [the gain of the amplifier changes when the switches (sw1a, swna, swnb, sw1b) are opened or closed], a plurality of resistive negative feedback circuits (112a, 112b) which are electrically connected to the input port (V_{ip}, V_{in}) and gain circuit (114) for keeping the input impedance of the amplifier substantially fixed (see lines 1-2 of the abstract), and an output port (- output of 114, + output of 114).

Regarding claim 2, a feedback signal (a signal coming out of the - output of 114 to the 116) is fed back to the input port (vip) via at least a corresponding resistive negative feedback circuit (112a).

Regarding claims 3 and 10, a switch device (sw1a) which is electrically connected to at least a predetermined resistive negative feedback circuit (112a).

Regarding claim 4, the gain circuit (114) are made out of the plurality of MOS transistors which are well known in the art.

Regarding claims 5 and 12, the plurality of resistive negative feedback circuits (112a, 112b) are respectively a resistor electrically connected to a capacitor [(110a, 110b) a resistor 112a is connected to a capacitor 110a and a resistor 112b is connected to a capacitor 110b].

Regarding claims 6 and 13, the amplifier 114 is a low noise amplifier.

Regarding claim 7, the limitations recited in the claim 7 are intended use of the invention.

Regarding claim 11, a plurality of switch devices (sw1a, swna, swnb, sw1b) which are respectively electrically connected to the plurality of resistive negative feedback circuits (112a, 112b).

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Patent numbers (5,486,791; 5,721,513) are the amplifier circuits with the feedback resistors and switches.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Henry Choe whose telephone number is (571) 272-1760.



**HENRY CHOE
PRIMARY EXAMINER**

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